



**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE**  
(UGC-AUTONOMOUS INSTITUTION)

Affiliated to JNTUA, Ananthapuramu & Approved by AICTE, New Delhi  
NAAC Accredited with A+ Grade, NIRF India Rankings 2022 - Band: 251-300 (Engg.)  
NBA Accredited - B.Tech. (CIVIL, CSE, CST, ECE, EEE, MECH), MBA & MCA  
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**Examination Branch**

**M. Tech (VLSI) – I Year I Semester (R24) Regular End Semester Examinations, March-2025**  
(For 2024 Admitted batch)

**TIME TABLE**

**Academic Year: 2024– 25**

**Time: 10:00 AM to 01:00 PM**

**Duration: 3 hours**

DATE / DAY	Course Name & Code
05.03.2025 (Wednesday)	CMOS Digital IC Design- 24VESP101
07.03.2025 (Friday)	Microcontrollers and Programmable Digital Signal Processors- 24VESP102
11.03.2025 (Tuesday)	FPGA Architectures and Applications-24VESP403
13.03.2025 (Thursday)	Low Power VLSI Design-24VESP404
15.03.2025 (Saturday)	Research Methodology and IPR-24RMP101

**Note:** (i) Any clashes or omissions in the time table may please be informed to the Controller of Examinations Immediately.  
(ii) Even if Government declares holiday on any of the above dates, the examination shall be conducted as usual.

*Controller of Examinations*

**CONTROLLER OF EXAMINATIONS**

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**Date: 28.01.2025**

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*Principal*

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**Principal**

**Madanapalle Institute of  
Technology & Science  
MADANAPALLE**

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